

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (currently ~~amended~~): A receiver comprising:

despreading code calculating means for calculating a code for despreading a reception signal based on delays given to the reception signal over a plurality of transmission paths through which the reception signal is transmitted and coefficients representing respective phase/amplitude ratios of the transmission paths;

despreading means for despreading the reception signal using the code calculated by and output directly from said despreading code calculating means; and

integrating means for integrating the reception signal despread by said despreading means.

2. (currently ~~amended~~): A receiver comprising:

memory means for storing a code for despreading a reception signal, calculated in advance based on delays given to the reception signal over a plurality of transmission paths through which the reception signal is transmitted and coefficients representing respective phase/amplitude ratios of the transmission paths;

despreading means for despreading the reception signal using the code stored by and output directly from said memory means; and

integrating means for integrating the reception signal despread by said despreding means.

3. (original): A receiver according to claim 1, wherein said despreding code calculating means comprises:

a plurality of delay means for adding the delays over the transmission paths to a complex conjugate value of a spreading code used when the reception signal is transmitted, and outputting delayed signals; a plurality of multiplying means for multiplying the delayed signals outputted from said delay means by complex conjugate values of the coefficients representing the respective phase/amplitude ratios of the transmission paths, and outputting product signals; and adding means for adding the product signals outputted from said multiplying means, and outputting the sum as the code for despreding the reception signal.

4. (original): A receiver according to claim 1, further comprising memory means for storing the code outputted from said despreding code calculating means, said despreding means comprising means for despreding said reception signal using the code stored by said memory means.

5. (original): A receiver according to claim 3, further comprising memory means for storing the code outputted from said adding means, said despreding means comprising means for despreding said reception signal using the code stored by said memory means.

6. (original): A receiver according to claim 3, wherein there are as many said delay means and said multiplying means as the number of the transmission paths.

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7. (original): A receiver according to claim 5, wherein there are as many said delay means and said multiplying means as the number of the transmission paths.
